

 **ICOM**[®]

SERVICE MANUAL

PMR446 FM TRANSCEIVER

IC-4088SR

INTRODUCTION

This service manual describes the latest service information for the **IC-4088SR** PMR446 FM TRANSCEIVER at the time of publication.

MODEL	Antenna size	SYMBOL
IC-4088SR	Long	EUR
	Short	EUR-1

To upgrade quality, all electrical or mechanical parts and internal circuits are subject to change without notice or obligation.

DANGER

NEVER connect the transceiver to an AC outlet or to a DC power supply that uses more than 5.0 V. Such a connection could cause a fire or electric hazard.

DO NOT expose the transceiver to rain, snow or any liquids.

DO NOT reverse the polarities of the power supply when connecting the transceiver.

DO NOT apply an RF signal of more than 20 dBm (100mW) to the antenna connector. This could damage the transceiver's front end.

ORDERING PARTS

Be sure to include the following four points when ordering replacement parts:

1. 10-digit order numbers
2. Component part number and name
3. Equipment model name and unit name
4. Quantity required

<SAMPLE ORDER>

5030002520 LCD L2-0494TAY IC-4088SR Main unit 5 pieces
8810009560 Screw PH BO M2x6 ZK IC-4088SR Chassis 10 pieces

Addresses are provided on the inside back cover for your convenience.



REPAIR NOTES

1. Make sure a problem is internal before disassembling the transceiver.
2. **DO NOT** open the transceiver until the transceiver is disconnected from its power source.
3. **DO NOT** force any of the variable components. Turn them slowly and smoothly.
4. **DO NOT** short any circuits or electronic parts. An insulated turning tool **MUST** be used for all adjustments.
5. **DO NOT** keep power ON for a long time when the transceiver is defective.
6. **DO NOT** transmit power into a signal generator or a sweep generator.
7. **ALWAYS** connect a 30 dB to 40 dB attenuator between the transceiver and a deviation meter or spectrum analyzer when using such test equipment.
8. **READ** the instructions of test equipment thoroughly before connecting equipment to the transceiver.

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SECTION 1 SPECIFICATIONS

■ GENERAL

• Number of channel	: 8 channels (simplex; 446.00625–446.09375 MHz)
• Type of emission	: 8K50F3E (FM)
• Frequency stability	: ± 2500 Hz (± 5.6 ppm)
• Frequency resolution	: 12.5 kHz
• Antenna impedance	: 50 Ω
• Power supply requirement (negative ground)	: 3 \times AA (R6) dry, alkaline; or BP-202 (optional Ni-Cd battery pack)
• Current drain	: Less than 140 mA
• Operating temperature range	: -20°C to $+55^{\circ}\text{C}$
• Number of CTCSS frequency	: 38 (67.0–250.3 Hz)
• Dimensions (projections not included)	: 52.5 (W) \times 102.5 (H) \times 26.9 (D) mm
• Weight (included 3 cells)	: 200 g (Approx.)

■ TRANSMITTER

• Output power	: Less than 500 mW ERP
• Modulation system	: Variable reactance frequency modulation
• Max. frequency deviation	: ± 2.5 kHz
• Spurious emissions	: 0.25 μW
• Adjacent channel power	: More than 60 dB
• External microphone connector	: 3-conductor 2.5(d) mm/2.2 k Ω

■ RECEIVER

• Receiving system	: Double conversion superheterodyne system
• Intermediate frequency	: 1st; 21.7 MHz 2nd; 450 kHz
• Sensitivity (12 dB SINAD)	: 0.25 μV ; -12 dB μ
• Adjacent channel selectivity	: More than 55 dB
• Spurious response	: More than 65 dB
• Intermodulation	: More than 60 dB
• Audio output power	: 100mW at 10% distortion with an 8 Ω load
• External SP connector	: 2-conductor 3.5(d) mm/8 Ω

All stated specifications are subject to change without notice or obligation.

■ CHANNEL FREQUENCY LIST

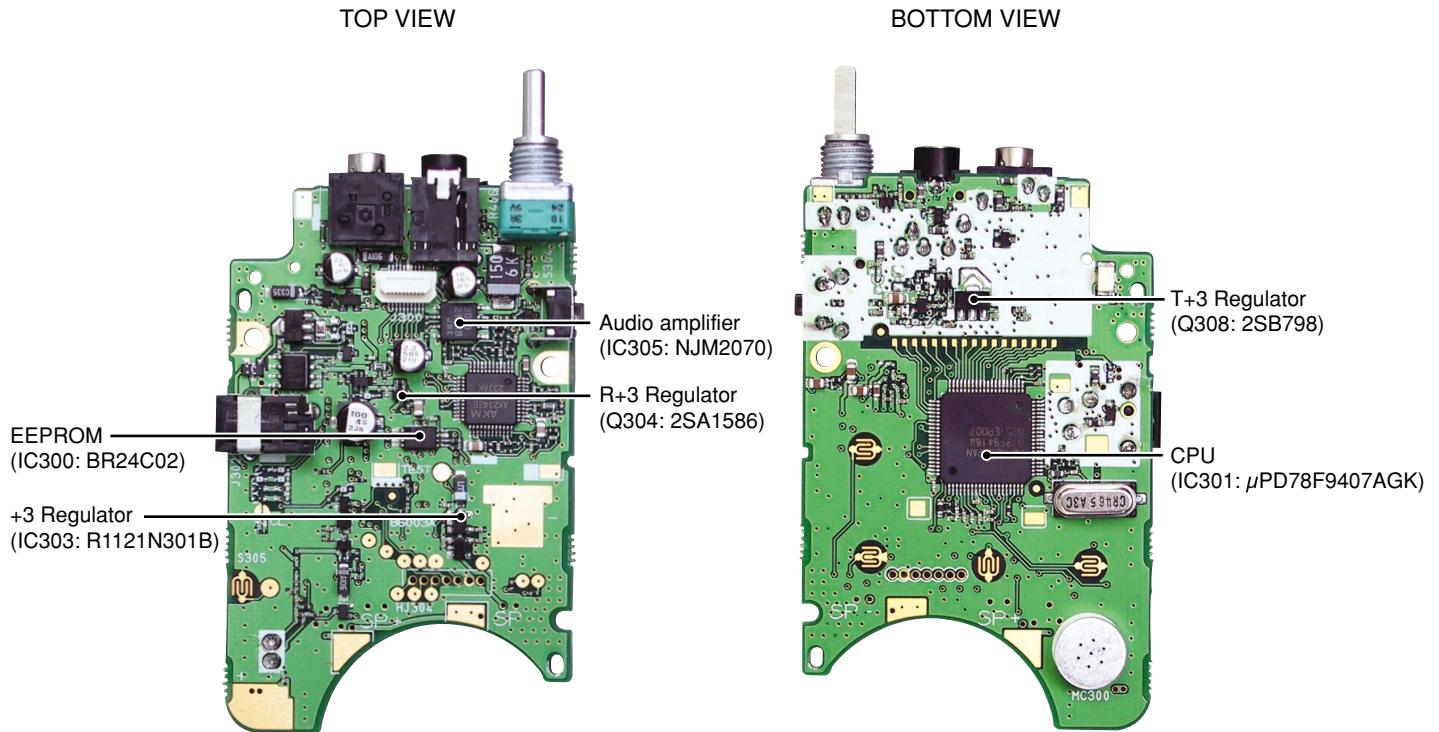
Channel No.	Frequency (MHz)	Channel No.	Frequency (MHz)
1	446.00625	5	446.05625
2	446.01875	6	446.06875
3	446.03125	7	446.08125
4	446.04375	8	446.09375

■ CTCSS FREQUENCY LIST

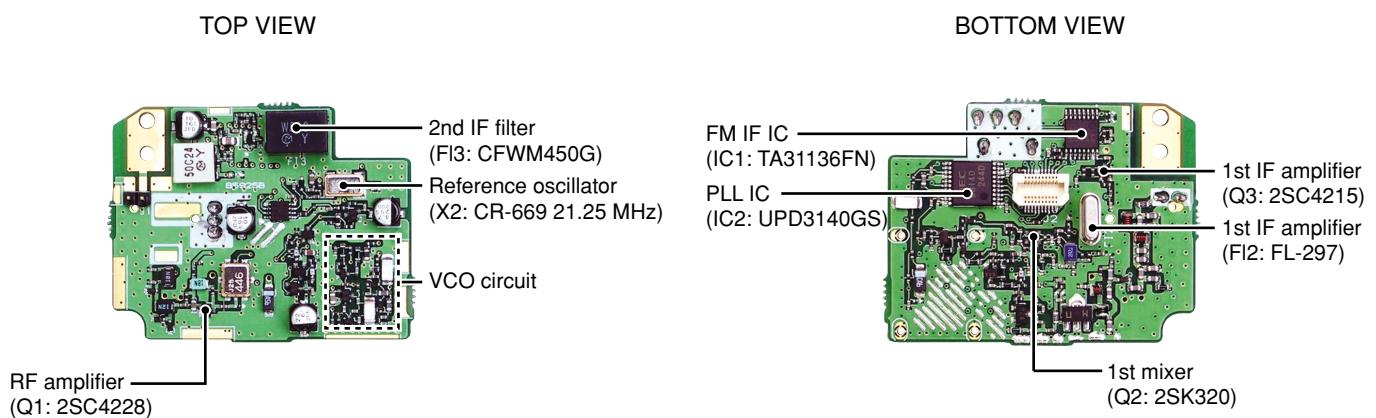
Channel No.	Frequency (Hz)	Channel No.	Frequency (Hz)	Channel No.	Frequency (Hz)
1	67.0	14	107.2	27	167.9
2	71.9	15	110.9	28	173.8
3	74.4	16	114.8	29	179.9
4	77.0	17	118.8	30	186.2
5	79.7	18	123.0	21	192.8
6	82.5	19	127.3	32	203.5
7	85.4	20	131.8	33	210.7
8	88.5	21	136.5	34	218.1
9	91.5	22	141.3	35	225.7
10	94.8	23	146.2	36	233.6
11	97.4	24	151.4	37	241.8
12	100.0	25	156.7	38	250.3
13	103.5	26	162.2	-	OFF

SECTION 2 INSIDE VIEWS

• MAIN UNIT



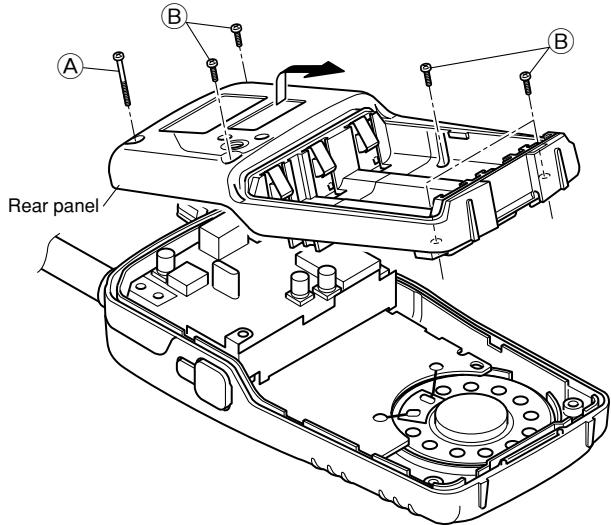
• RF UNIT



SECTION 3 DISASSEMBLY INSTRUCTIONS

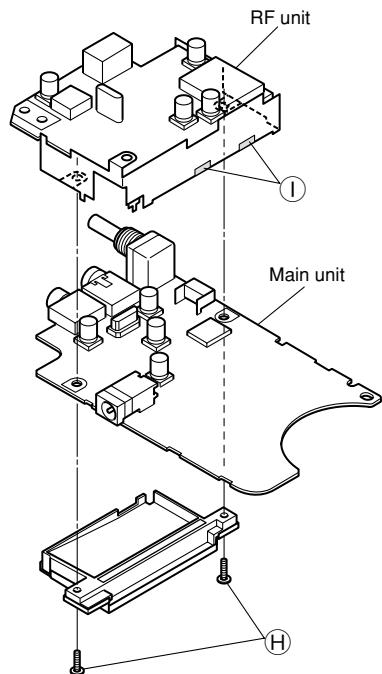
• REMOVING THE REAR PANEL

- ① Unscrew 1 screw (A), and 5 screws (B).
- ② Remove the rear panel in the direction of the arrow.



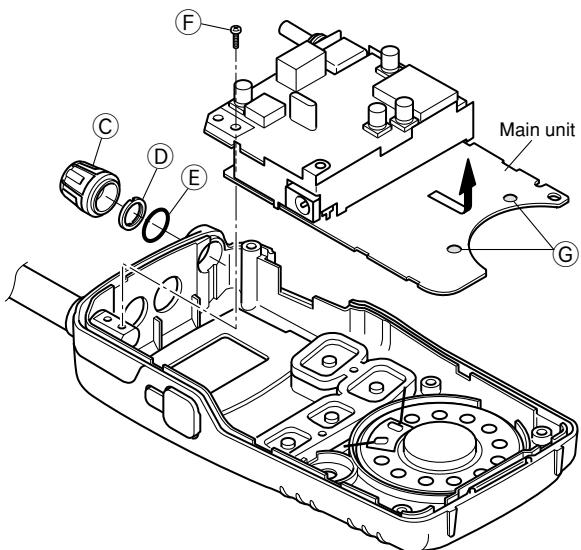
• REMOVING THE RF UNIT

- ① Unscrew 2 screws (H).
- ② Unsolder 2 points (I), and remove the RF unit.



• REMOVING THE MAIN UNIT

- ① Remove 1 knob (C), unscrew 1 nut (D), and then remove 1 ring (E).
- ② Unscrew 1 screw (F).
- ③ Unsolder 2 points (G), and remove the MAIN unit in the direction of the arrow.



SECTION 4 CIRCUIT DESCRIPTION

4-1 RECEIVER CIRCUITS

4-1-1 ANTENNA SWITCHING CIRCUIT (RF UNIT)

Received signals from the antenna are passed through the low-pass filter (L2, L3, C1–C5, C117). The filtered signals are applied to the $\lambda/4$ type antenna switching circuit (D1–D4, L4–L6, C6–C11).

The antenna switching circuit functions as a low-pass filter while receiving. However, its impedance becomes very high while D3 and D4 are turned ON (while transmitting). Thus, transmit signals are blocked from entering the receiver circuits. The passed signals are then applied to the RF amplifier circuit.

4-1-2 RF CIRCUIT (RF UNIT)

The RF circuit amplifies signals within the range of frequency coverage and filters out-of-band signals.

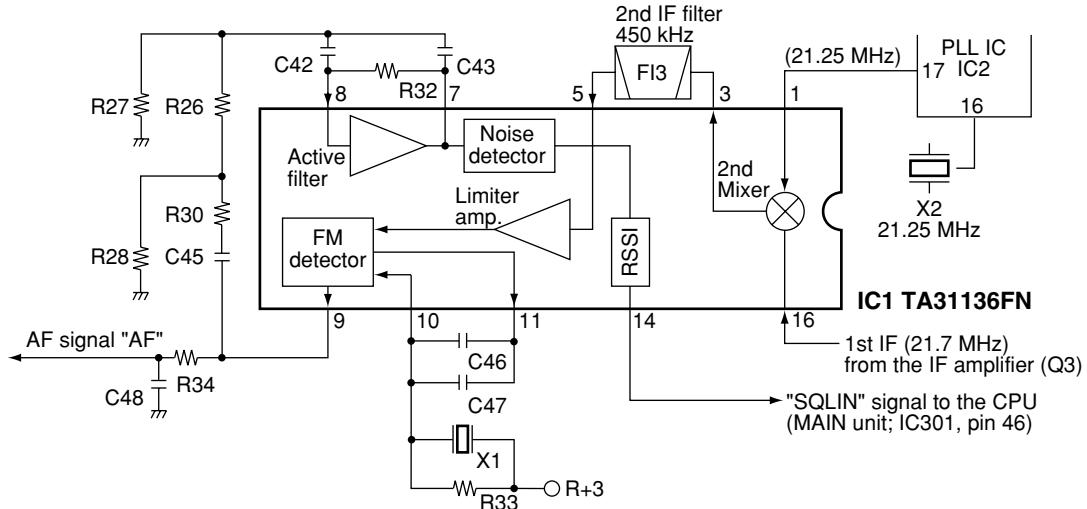
The signals from the antenna switching circuit are amplified at the RF amplifier (Q1) and passed through the bandpass filter (FI4) to suppress out-of-band signals. The filtered signals are applied to the 1st mixer circuit (Q2).

4-1-3 1ST MIXER AND 1ST IF CIRCUITS (RF UNIT)

The 1st mixer circuit converts the received signals to a fixed frequency of the 1st IF signal with a PLL output frequency. By changing the PLL frequency, only desired signals will be passed through a crystal filter at the next stage of the 1st mixer.

The signals from the bandpass filter are mixed at the 1st mixer circuit (Q2) with a 1st LO signal coming from the VCO circuit to produce a 21.7 MHz 1st IF signal. The 1st IF signal is applied to a crystal filter (FI2) to suppress out-of-band signals. The filtered 1st IF signal is applied to the IF amplifier (Q3), and is then applied to the 2nd mixer circuit (IC1, pin 16).

• 2ND IF AND DEMODULATOR CIRCUITS



4-1-4 2ND MIXER AND DEMODULATOR CIRCUITS (RF UNIT)

The 2nd mixer circuit converts the 1st IF signal to a 2nd IF signal. A double conversion superheterodyne system (which converts receive signals twice) improves the image rejection ratio and obtains stable receiver gain.

The 1st IF signal from the IF amplifier (Q3) is applied to the 2nd mixer section in the FM IF IC (IC1, pin 16), and is mixed with the 2nd LO signal to be converted into a 450 kHz 2nd IF signal.

The FM IF IC contains a 2nd mixer, quadrature detector, noise amplifier and a limiter amplifier, etc. The PLL reference oscillator (X2) is used for the 2nd LO signal via the PLL IC (IC2, pins 16, 17), and is applied to pin 1 of the FM IF IC (IC1).

The mixed 2nd IF signal is output from pin 3 and passed through the ceramic bandpass filter (FI3) to remove unwanted heterodyne frequencies. It is then amplified at the limiter amplifier section (IC1, pin 5), and is applied to the quadrature detector section (IC1, pins 10, 11) to demodulate the 2nd IF signal into AF signals.

4-1-5 AF CIRCUIT (RF AND MAIN UNITS)

AF signals from the FM IF IC (RF unit; IC1, pin 9) are applied to the MAIN unit via J300 pin 14.

The AF signal applied to the [VOL] control (MAIN unit; R406) to control the audio level via the volume mute switch (MAIN unit; Q316). The level controlled AF signals are applied to the AF power amplifier (MAIN unit; IC305, pin 2) to drive an internal speaker (CHASSIS unit; SP1) via the [SP] jack (MAIN unit; J301).

4-1-6 SQUELCH CIRCUIT (RF AND MAIN UNITS)

(1) NOISE SQUELCH

The noise squelch circuit cuts out AF signals when no RF signals are received. By detecting noise components in the AF signals, the squelch circuit switches the AF mute switch.

A portion of the AF signals from the FM IF IC (RF unit; IC1, pin 9) are applied to the active filter section (RF unit; IC1, pin 8). The active filter section amplifies and filters noise components. The filtered signals are applied to the noise detector section and output from pin 14 as the "SQLIN" signal.

The "SQLIN" signal from IC1 (pin 14) on the RF unit passes through J300, pin 12, and is then applied to the CPU (MAIN unit; IC301, pin 46). The CPU analyzes the noise condition and outputs the "RMUT" (from pin 64) and "AFON" (from pin 58) signals to toggle the AF mute (MAIN unit; Q316) and volume mute (MAIN unit; Q311) switches.

(2) TONE SQUELCH

The tone squelch circuit detects AF signals and opens the squelch only when receiving a signal containing a matching subaudible tone (CTCSS). When tone squelch is in use, and a signal with a mismatched or no subaudible tone is received, the tone squelch circuit mutes the AF signals even when noise squelch is open.

A portion of the AF signals from the FM IF IC (RF unit; IC1, pin 9) passes through the AF control (MAIN unit; IC304) to control the volume mute and AF mute switches.

4-2 TRANSMITTER CIRCUITS

4-2-1 MICROPHONE AMPLIFIER CIRCUIT (MAIN UNIT)

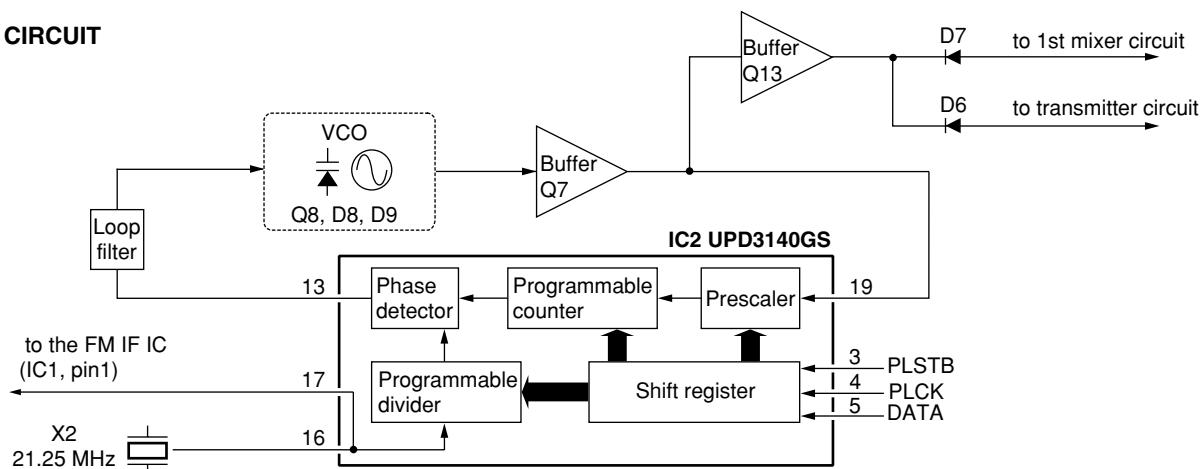
AF signals from the internal/external microphone are applied to the microphone amplifier circuit and low-pass filter (IC304) via the microphone switch (Q313). The filtered audio signals are applied to the modulation circuit on the RF unit via J300, pin 15 as the "MOD" signal.

4-2-2 MODULATION CIRCUIT (RF UNIT)

The filtered audio signals from J300, pin 15 (on the MAIN unit) are applied to the modulation circuit (D8, D9) to modulate transmit signals at the VCO circuit (Q8).

The modulated signal is applied to the drive amplifier circuit.

• PLL CIRCUIT



4-2-3 DRIVE/POWER AMPLIFIER CIRCUITS (RF UNIT)

The drive/power amplifier circuit amplifies the VCO oscillating signal to the output power level.

The modulated transmit signal is amplified at the drive amplifiers (Q5, Q6) after being amplified at the buffer amplifiers (Q7, Q13). The amplified signal is amplified at the power amplifier (Q4) to obtain 450 mW of RF power.

The signal is passed through the antenna switching circuit (D1) and low-pass filter, and is then applied to the antenna.

4-3 PLL CIRCUITS (RF UNIT)

A PLL circuit provides stable oscillation of the transmit frequency and receive 1st LO frequency. The PLL output compares the phase of the divided VCO frequency to the reference frequency. The PLL output frequency is controlled by the divided ratio (N-data) of a programmable divider.

The PLL circuit consists of the VCO circuit (Q8, D8, D9). An oscillated signal from the VCO passes through the buffer amplifier (Q7), and is applied to the PLL IC (IC2, pin 19) to prescale in the PLL IC based on the divided ratio (N-data). The reference signal is generated at the reference oscillator (X2), and is applied to the PLL IC. The PLL IC detects the out-of-step phase using the reference frequency, and outputs detected signal from pin 14. The output signal is passed through the loop filter (R44, C67) and is then applied to the VCO circuit as lock voltage.

If the oscillated signal drifts, its phase changes from that of the reference frequency, causing a lock voltage change to compensate for the drift in the oscillated frequency.

4-4 POWER SUPPLY CIRCUITS

VOLTAGE LINE

LINE	DESCRIPTION
VCC	The voltage from the connected battery or power supply from DC IN (J302).
C3V	Common 3 V converted from the VCC line at the +3V regulator circuit (IC303). The circuit outputs the voltage regardless of the power ON/OFF condition.
+3V	Common 3 V converted from the VCC line at the +3V current amplifier (Q303, D301).
R+3	Receive 3 V controlled by the R+3 current amplifier (Q304) using the "RXC" signal from the CPU (IC301, pin 59).
T+3	Transmit 3 V converted from the VCC line at the T+3 current amplifier (Q305, Q308, Q319, D302, D303, D305) using the "TXC" signal from the CPU (IC301, pin 65).

4-5 PORT ALLOCATIONS

4-5-1 D/A CONVERTER (RF unit; IC3)

Pin number	Port name	Description
1	MODC	Outputs the deviation control signal.
2	PWRC	Outputs the TX power control signal.
3	FC	Outputs the PLL reference frequency control signal.

4-5-2 CPU (MAIN unit; IC301)

Pin number	Port name	Description
27	TEST	Input port for the test mode switch.
31	EEPCK	Outputs the clock signal to the EEPROM (MAIN unit; IC300).
32	EEPDA	Outputs the data signals to the EEPROM (MAIN unit; IC300).
33	DETOUT	Input port for the control signal of the tone detection
35	SHIFT	Outputs the shift control signal to the PLL IC (RF unit; IC2).
36	PLSTB	Outputs the strobe signals to the PLL IC (RF unit; IC2).
37	CK	Outputs the clock signal to the PLL IC (RF unit; IC2).
38	DATA	Outputs the data signals to the PLL IC (RF unit; IC2).

Pin number	Port name	Description
41	UNLK	Input port for the PLL unlock signal from the PLL IC (RF unit; IC2). Low : During unlock.
42	PTT	Input port for the PTT switch from the external mic jack (MAIN unit; J303). Low : External microphone PTT switch is pushed.
43	MICIN	Input port for the control signal from the external remote microphone.
44	TEMP	Input port for the transceiver's internal temperature.
46	SQLIN	Input port for the squelch level signal.
47	VIN	Input port for connected battery pack voltage.
51	MODE	Input port for [MODE] switch.
52	POWSW	Input port for [POWER] switch. Low : While [POWER] switch is pushed.
53	BEEP	Outputs the beep audio signal.
54	DOWN	Input port for [DOWN] switch.
55	UP	Input port for [UP] switch.
56	PTTSW	Input port for the internal PTT switch. Low : While [PTT] switch is pushed.
57	LAMPO	Outputs the control signal of LCD back light.
58	AFON	Outputs the control signal of the AF amplifier regulator circuit (MAIN unit; Q306, Q307, Q311).
59	RXV	Outputs the control signal of the R+3 regulator circuit (MAIN unit; Q304).
61	MMUTE	Outputs the microphone mute signal for RING function. High : While RING signals are output, etc.
62	MICSW	Outputs the internal microphone control signal. High : While the internal PTT switch is pushed.
63	POWER	Outputs the control signal of the +3V regulator circuit (MAIN unit; Q303, D301).
64	RMUTE	Outputs the control signal of the volume mute switch (MAIN unit; Q316). High : While squelched.
65	TXV	Outputs the control signal of the T+3 regulator circuit (Q305, Q308, D303, D304).
66	CHGC	Outputs the control signal of the charge circuit (Q312, Q314, D305, D308).

SECTION 5 ADJUSTMENT PROCEDURES

5-1 PREPARATION

- When adjusting the IC-4088SR, *HM-75A JIG MICROPHONE (modified HM-75A OPTIONAL SPEAKER-MICROPHONE; see illustration at page5-2) is required.
- All adjustment items must be preformed at "ADJUSTMENT MODE". (See below in detail)

■ REQUIRED TEST EQUIPMENT

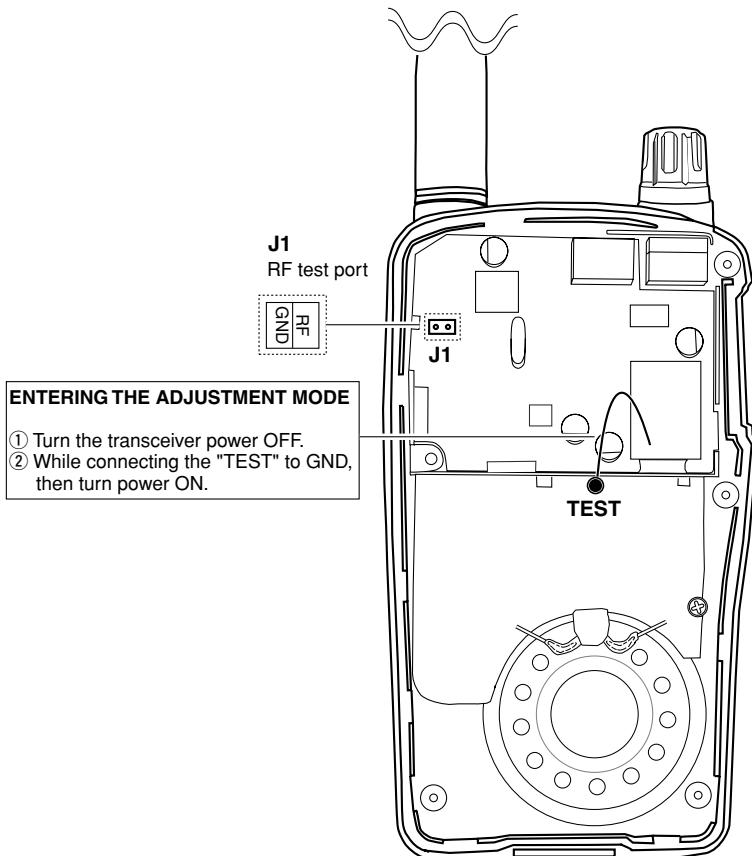
EQUIPMENT	GRADE AND RANGE	EQUIPMENT	GRADE AND RANGE
DC power supply	Output voltage : 4.5 V DC Current capacity : 1 A or more	FM deviation meter	Frequency range : 30–600 MHz Measuring range : 0 to ±10 kHz
RF power meter (terminated type)	Measuring range : 1 mW–1 W Frequency range : 300–600 MHz Impedance : 50 Ω SWR : Less than 1.2 : 1	Standard signal generator (SSG)	Frequency range : 0.1–600 MHz Output level : 0.1 μV–32 mV (-127 to -17 dBm)
	AC millivoltmeter	Measuring range : 10 mV–10 V	
Frequency counter	Frequency range : 0.1–600 MHz Frequency accuracy : ±1 ppm or better Sensitivity : 100 mV or better	Audio generator	Frequency range : 300–3000 Hz Output level : 1–500 mV

■ ENTERING THE ADJUSTMENT MODE

- Turn the transceiver's power OFF.
- While connecting the "TEST" on the MAIN unit to "GND", then turn power ON.

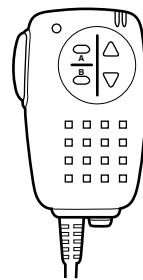
• DISPLAYED CHANNEL'S FREQUENCY LIST

CHANNEL NO.	FREQUENCY
1ch.	446.00625 MHz
2ch.	446.01875 MHz
3ch.	446.03125 MHz
4ch.	446.04375 MHz
5ch.	446.05625 MHz
6ch.	446.06875 MHz
7ch.	446.08125 MHz
8ch.	446.09375 MHz



■ OPERATION ON THE ADJUSTMENT MODE

- Change the adjustment item : HM-75A's [B] key
- Change the adjustment value : HM-75A's [Δ]/[∇] keys
- Verify the adjustment value : HM-75A's [A] key
- Change the adjustment channel : IC-4088SR's [UP]/[DN] keys
- Change the adjustment group : IC-4088SR's [MODE]+ [UP]/[DN] keys

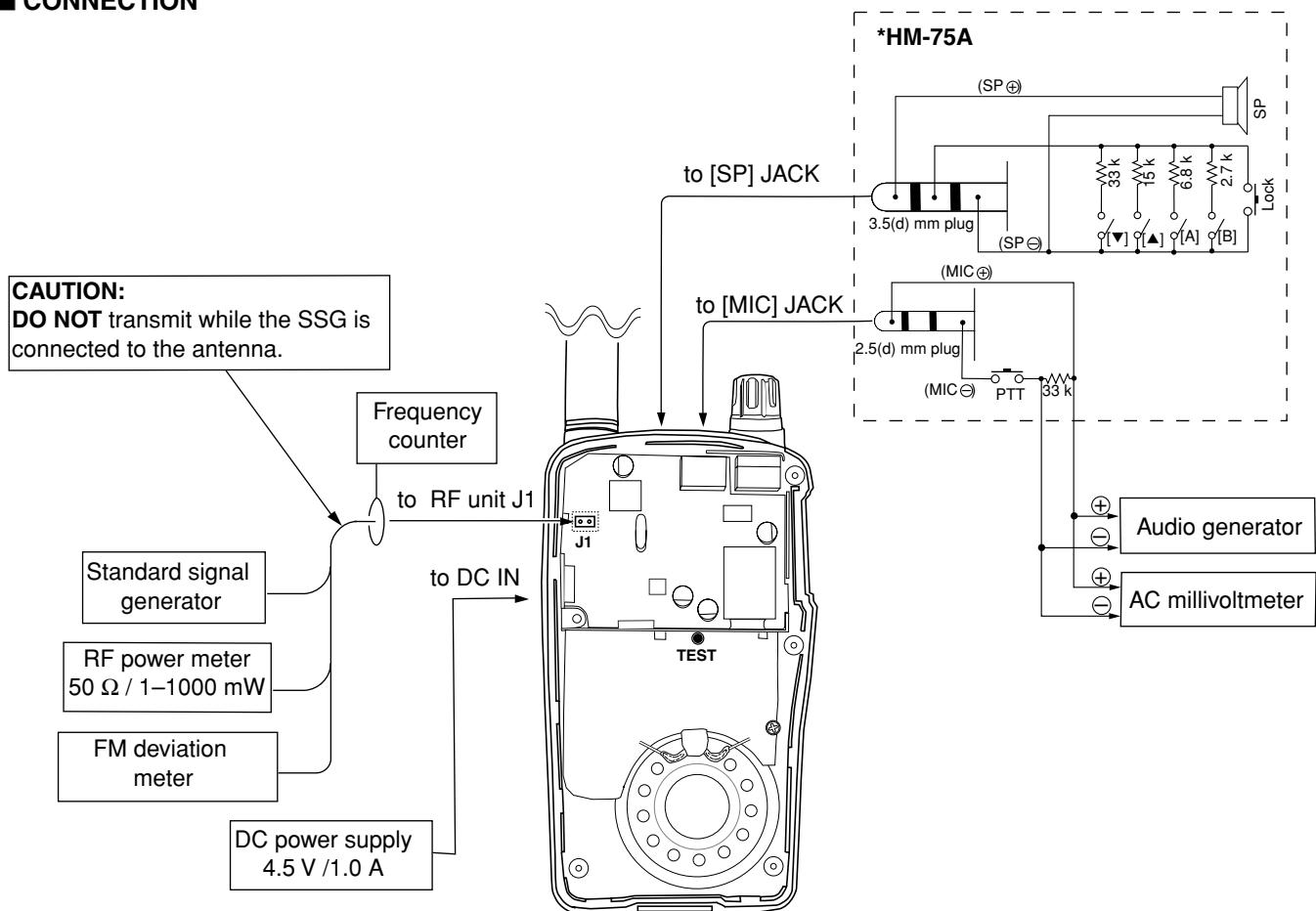


■ ADJUSTMENT ITEMS

When entering adjustment mode, displayed adjustment items indicator on LCD as follow.

- | | | |
|----------------------------------|-------------|-------------------------|
| • Reference frequency adjustment | : Displayed | <i>F_r-88</i> |
| • Output power adjustment | : Displayed | <i>P_L-88</i> |
| • FM deviation adjustment | : Displayed | <i>dL-88</i> |
| • CTCSS adjustment | : Displayed | <i>L_C-88</i> |
| • Squelch adjustment | : Displayed | <i>SQ-88</i> |

■ CONNECTION



5-2 ADJUSTMENT MODE ADJUSTMENTS

The following adjustment must be performed at "ADJUSTMENT MODE".

ADJUSTMENT		ADJUSTMENT CONDITION	MEASUREMENT	VALUE	HM-75A's KEY												
REFERENCE FREQUENCY [Fr]	1	<ul style="list-style-type: none"> • Operating channel : Ch 8 • Transmitting 	Loosely couple a frequency counter to the antenna.	446.09375 MHz	[▲]/[▼]												
OUTPUT POWER [PL]	1	<ul style="list-style-type: none"> • Operating channel : Ch 8 • Transmitting 	Connect an RF power meter to the RF test port J1.	450 mW	[▲]/[▼]												
FM DEVIATION [dL]	1	<ul style="list-style-type: none"> • Operating channel : Ch 8 • Connect an audio generator to the [MIC] jack and set as : 1 kHz/100 mV rms • Set an FM deviation meter as: <table style="margin-left: 20px; border-collapse: collapse;"> <tr><td>HPF</td><td>: OFF</td></tr> <tr><td>LPF</td><td>: 20 kHz or 15 kHz</td></tr> <tr><td>De-emphasis</td><td>: OFF</td></tr> <tr><td>Detector</td><td>: (P-P)/2</td></tr> <tr><td>• Set group No.</td><td>: OFF (--)</td></tr> <tr><td>• Transmitting</td><td></td></tr> </table> 	HPF	: OFF	LPF	: 20 kHz or 15 kHz	De-emphasis	: OFF	Detector	: (P-P)/2	• Set group No.	: OFF (--)	• Transmitting		Connect an FM deviation meter to the RF test port J1.	± 2.05 kHz	[▲]/[▼]
HPF	: OFF																
LPF	: 20 kHz or 15 kHz																
De-emphasis	: OFF																
Detector	: (P-P)/2																
• Set group No.	: OFF (--)																
• Transmitting																	
CTCSS MODULATION [to]	2	<ul style="list-style-type: none"> • Operating channel : Ch 1 • Set group No. : 24 • Transmitting 		± 0.30 kHz	[▲]/[▼]												
SQUELCH SENSITIVITY [Sq]	1	<ul style="list-style-type: none"> • Operating channel : Ch 1 • Connect an SSG to J1 on the RF unit and set as: <table style="margin-left: 20px; border-collapse: collapse;"> <tr><td>Level</td><td>: 0.13 µV*</td></tr> <tr><td></td><td>(-125 dBm)</td></tr> <tr><td>Modulation</td><td>: OFF</td></tr> <tr><td>• Receiving</td><td></td></tr> </table> 	Level	: 0.13 µV*		(-125 dBm)	Modulation	: OFF	• Receiving		speaker	<ul style="list-style-type: none"> • Squelch sensitivity is adjusted automatically when HM-75A's [A] button is pushed. 					
Level	: 0.13 µV*																
	(-125 dBm)																
Modulation	: OFF																
• Receiving																	
	2	<ul style="list-style-type: none"> • Set an SSG as: <table style="margin-left: 20px; border-collapse: collapse;"> <tr><td>Level</td><td>: OFF</td></tr> <tr><td>• Receiving</td><td></td></tr> </table> 	Level	: OFF	• Receiving		Audio signal disappears	Verify									
Level	: OFF																
• Receiving																	

SECTION 6 PARTS LIST

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.
IC300	1130011230	S.I.C	BR24C02FV-WE2
IC301	1140011430	S.I.C	μPD789407AGK-A81-9EU (FX2624A3)
IC302	1110005820	S.I.C	R3112N281A-TR
IC303	1180002430	S.REG	R1121N301B-TR
IC304	1130007382	S.I.C	AK2342B
IC305	1110002810	S.I.C	NJM2070M-T1
Q302	1590001330	S.TRANZISTOR	DTA114EUA T106
Q303	1590002490	S.TRANZISTOR	UMZZN TR
Q304	1510000770	S.TRANZISTOR	2SA1586-GR (TE85R)
Q305	1530002690	S.TRANZISTOR	2SC4116-GR (TE85R)
Q306	1530002690	S.TRANZISTOR	2SC4116-GR (TE85R)
Q307	1510000580	S.TRANZISTOR	2SA1362-GR (TE85R)
Q308	1520000200	S.TRANZISTOR	2SB798-T2 DK
Q309	1590001050	S.TRANZISTOR	DTC114TUA T106
Q310	1590001040	S.TRANZISTOR	DTA113ZU T106
Q311	1530003090	S.TRANZISTOR	2SC4213-B (TE85R)
Q312	1590000430	S.TRANZISTOR	DTC144EUA T106
Q313	1560000840	S.FET	2SK1829 (TE85R)
Q314	1520000450	S.TRANZISTOR	2SB1132 T100 Q
Q315	1590002950	S.FET	HAT1023R-EL
Q316	1590001390	S.FET	2SJ144-Y (TE85R)
Q317	1590000430	S.TRANZISTOR	DTC144EUA T106
Q318	1590000850	S.TRANZISTOR	DTC114YUA T106
Q319	1590001650	S.TRANZISTOR	XP4601 (TX)
D301	1790001250	S.DIODE	MA2S111-(TX)
D302	1790001250	S.DIODE	MA2S111-(TX)
D303	1790001250	S.DIODE	MA2S111-(TX)
D305	1790000670	S.DIODE	SB07-03C-TB
D306	1750000880	S.DIODE	RB551V-30TE-17
D307	1790001240	S.DIODE	MA2S728-(TX)
D308	1790000860	S.DIODE	MA133 (TX)
D310	1790001250	S.DIODE	MA2S111-(TX)
D313	1790001250	S.DIODE	MA2S111-(TX)
D314	1790001250	S.DIODE	MA2S111-(TX)
D317	1790001250	S.DIODE	MA2S111-(TX)
D318	1750000880	S.DIODE	RB551V-30TE-17
X300	6050008860	S.XTAL	CR-465 (3.6864 MHz)

[MAIN UNIT]

REF NO.	ORDER NO.	DESCRIPTION	M.
R369	7030007570	S.RESISTOR	ERJ2GEJ 122X (1.2 kΩ)
R373	7030005080	S.RESISTOR	ERJ2GEJ 823 X (82 kΩ)
R374	7030009140	S.RESISTOR	ERJ2GEJ 272 X (2.7 kΩ)
R375	7030007310	S.RESISTOR	ERJ2GEJ 155 X (1.5 MΩ)
R376	7030007570	S.RESISTOR	ERJ2GEJ 122X (1.2 kΩ)
R377	7030007340	S.RESISTOR	ERJ2GEJ 153 X (15 kΩ)
R378	7030005230	S.RESISTOR	ERJ2GEJ 334 X (330 kΩ)
R379	7030004990	S.RESISTOR	ERJ2GEJ 221 X (220 Ω)
R381	7030005600	S.RESISTOR	ERJ2GEJ 273 X (27 kΩ)
R382	7030008010	S.RESISTOR	ERJ2GEJ 123 X (12 kΩ)
R383	7030005710	S.RESISTOR	ERJ2GEJ 121 X (120 Ω)
R384	7030005090	S.RESISTOR	ERJ2GEJ 104 X (100 kΩ)
R385	7030004050	S.RESISTOR	ERJ3GEYJ 1R0 V (1 Ω)
R386	7030004980	S.RESISTOR	ERJ2GEJ 101 X (100 Ω)
R387	7030005040	S.RESISTOR	ERJ2GEJ 472 X (4.7 kΩ)
R388	7030005160	S.RESISTOR	ERJ2GEJ 105 X (1 MΩ)
R389	7030005160	S.RESISTOR	ERJ2GEJ 105 X (1 MΩ)
R390	7030005090	S.RESISTOR	ERJ2GEJ 104 X (100 kΩ)
R391	7030005050	S.RESISTOR	ERJ2GEJ 103 X (10 kΩ)
R392	7030005040	S.RESISTOR	ERJ2GEJ 472 X (4.7 kΩ)
R393	7030005570	S.RESISTOR	ERJ2GEJ 820 X (82 Ω)
R394	7030009290	S.RESISTOR	ERJ2GEJ 562 X (5.6 kΩ)
R395	7030005090	S.RESISTOR	ERJ2GEJ 104 X (100 kΩ)
R396	7030005160	S.RESISTOR	ERJ2GEJ 105 X (1 MΩ)
R397	703000130	S.RESISTOR	MCR10EZHZ 8.2 Ω (8R2)
R399	7030005530	S.RESISTOR	ERJ2GEJ 100 X (10 Ω)
R400	7030005040	S.RESISTOR	ERJ2GEJ 472 X (4.7 kΩ)
R401	7030005090	S.RESISTOR	ERJ2GEJ 104 X (100 kΩ)
R402	7030007300	S.RESISTOR	ERJ2GEJ 332 X (3.3 kΩ)
R403	7030004980	S.RESISTOR	ERJ2GEJ 101 X (100 Ω)
R404	7030005060	S.RESISTOR	ERJ2GEJ 333 X (33 kΩ)
R405	7030004980	S.RESISTOR	ERJ2GEJ 101 X (100 Ω)
R406	7210001970	VARIABLE	RV-244 (RK0971110) 10KA
R409	7030005090	S.RESISTOR	ERJ2GEJ 104 X (100 kΩ)
R410	7030005090	S.RESISTOR	ERJ2GEJ 104 X (100 kΩ)
R411	7030008280	S.RESISTOR	ERJ2GEJ 271 X (270 Ω)
R412	7030005050	S.RESISTOR	ERJ2GEJ 103 X (10 kΩ)
R413	7030005050	S.RESISTOR	ERJ2GEJ 103 X (10 kΩ)
R414	7030005050	S.RESISTOR	ERJ2GEJ 103 X (10 kΩ)
R415	7510001660	S.THERMISTOR	NTCG16 4LH 473KT
R416	7030005090	S.RESISTOR	ERJ2GEJ 104 X (100 kΩ)
R417	7030005240	S.RESISTOR	ERJ2GEJ 473 X (47 kΩ)
R418	7030005040	S.RESISTOR	ERJ2GEJ 472 X (4.7 kΩ)
R419	7030005090	S.RESISTOR	ERJ2GEJ 104 X (100 kΩ)
R420	7030007320	S.RESISTOR	ERJ2GEJ 225 X (2.2 MΩ)
R421	7030005050	S.RESISTOR	ERJ2GEJ 103 X (10 kΩ)
R422	7030005040	S.RESISTOR	ERJ2GEJ 472 X (4.7 kΩ)
R423	7030005160	S.RESISTOR	ERJ2GEJ 105 X (1 MΩ)
R424	7030005240	S.RESISTOR	ERJ2GEJ 473 X (47 kΩ)
R426	7030007290	S.RESISTOR	ERJ2GEJ 222 X (2.2 kΩ)
R427	7030007320	S.RESISTOR	ERJ2GEJ 225 X (2.2 MΩ)
R428	7030007060	S.RESISTOR	ERJ2GEJ 684X (680 kΩ)
R430	7030005100	S.RESISTOR	ERJ2GEJ 154 X (150 kΩ)
R431	7030005220	S.RESISTOR	ERJ2GEJ 223 X (22 kΩ)
R432	7030005240	S.RESISTOR	ERJ2GEJ 473 X (47 kΩ)
R433	7030005600	S.RESISTOR	ERJ2GEJ 273 X (27 kΩ)
C300	4030017410	S.CERAMIC	ECJ0EC1H240J
C301	4030017410	S.CERAMIC	ECJ0EC1H240J
C303	4030016790	S.CERAMIC	ECJ0EB1C103K
C304	4030016930	S.CERAMIC	ECJ0EB1A104K
C311	4030016930	S.CERAMIC	ECJ0EB1A104K
C312	4030016930	S.CERAMIC	ECJ0EB1A104K
C313	4030017430	S.CERAMIC	ECJ0EC1H101J
C314	4030016930	S.CERAMIC	ECJ0EB1A104K
C315	4550006620	S.TANTALUM	ECSTOJY226R
C316	4030009660	S.CERAMIC	C1608 JF 1C 224Z-T
C317	4030016930	S.CERAMIC	ECJ0EB1A104K
C319	4030016930	S.CERAMIC	ECJ0EB1A104K
C320	4030008680	S.CERAMIC	C2012 JF 1C 105Z-T
C321	4030008680	S.CERAMIC	C2012 JF 1C 105Z-T
C323	4030017040	S.CERAMIC	ECJ0EB1A333K
C324	4030016930	S.CERAMIC	ECJ0EB1A104K
C325	4030008680	S.CERAMIC	C2012 JF 1C 105Z-T
C326	4030008680	S.CERAMIC	C2012 JF 1C 105Z-T
C327	4030017720	S.CERAMIC	ECJ0EB1H331K
C328	4510005900	S.ELECTROLYTIC	ECEVOGA101SR
C329	4030017420	S.CERAMIC	ECJ0EC1H470J
C330	4030016930	S.CERAMIC	ECJ0EB1A104K
C331	4030016930	S.CERAMIC	ECJ0EB1A104K

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)

S.=Surface mount

[RF UNIT]

REF NO.	ORDER NO.	DESCRIPTION		M.
W5	7030010040	S.RESISTOR	ERJ2GE-JPW	B
W6	7030010040	S.RESISTOR	ERJ2GE-JPW	B
W7	7030003860	S.RESISTOR	ERJ3GE JPW V	B
W8	7030010040	S.RESISTOR	ERJ2GE-JPW	B
EP1	0910056422	PCB	B 5925B	
EP2	6910013310	S.BEAD	MMZ1608D121B	B

M.=Mounted side (T: Mounted on the Top side, B: Mounted on the Bottom side)
S.=Surface mount

SECTION 7 MECHANICAL PARTS AND DISASSEMBLY

7-1 CABINET PARTS [CHASSIS PARTS]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
SP1	2510001120	Speaker SDRS-3650P-008	1
EP1	3310003060	Antenna 2628 ANT [EUR]	1
	3310003120	Antenna 2628 SHORT ANT M [EUR-1]	1
MP1	8210019370	2628 Front panel	1
MP2	8110007900	2628 Rear cover	1
MP3	8210019380	2628 Rear panel	1
MP4	8310054740	2628 Window plate	1
MP5	8930059220	2628 Window sheet	1
MP6	8930059210	2628 SP JACK CAP	1
MP7	8610011270	Knob N-296	1
MP8	8930059240	2628 4-key	1
MP9	8930059250	2628 PTT rubber	1
MP10	8930059260	2628 A-terminal	1
MP11	8930059270	2628 B-terminal	1
MP12	8930059280	2628 Detect button	1
MP13	8930059550	2628 PTT sheet	1
MP14	8110007910	2628 Lock cover	1
MP15	8930059290	O-ring (AU)	1
MP16	8930059590	O-ring (AV)	1
MP17	8930045220	2045 BATT seal	1
MP19	8930045370	2045 C-terminal	2
MP20	8810008750	Screw PH B0 2x15 ZK (BT)	1
MP22	8930058310	2605 DC cap	1
MP25	8830000550	Nut (E)	1
MP32	8810009560	Screw PH B0 2x6 ZK (BT)	5
MP33	8810008620	Screw PH B0 2x20 ZK (BT)	1
MP35	8930059600	2628 Mic rubber	1
MP36	8930059610	Sponge (HC)	1

[MAIN UNIT]

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
R406	7210001970	RV-244	1
J301	6450001060	HSJ1493-01-01	1
J302	6450002130	04-730A-02BKA	1
J303	6450000130	HSJ1102-01-540	1
DS300	5030002520	LCD L2-0494TAY	1
MC300	7700002160	KUC3523-040245	1
S304	2260001900	SW-149	1
EP303	6910012350	LCD contact SRCN-2045-SP-N-W	1
MP301	8210019390	2628 Reflector	1
MP302	8930060120	2628 LCD holder	1
MP303	8810004890	Screw PH 2x6 ZK	2
MP304	8010019180	2628 CHASSIS	1

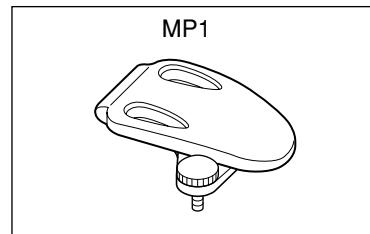
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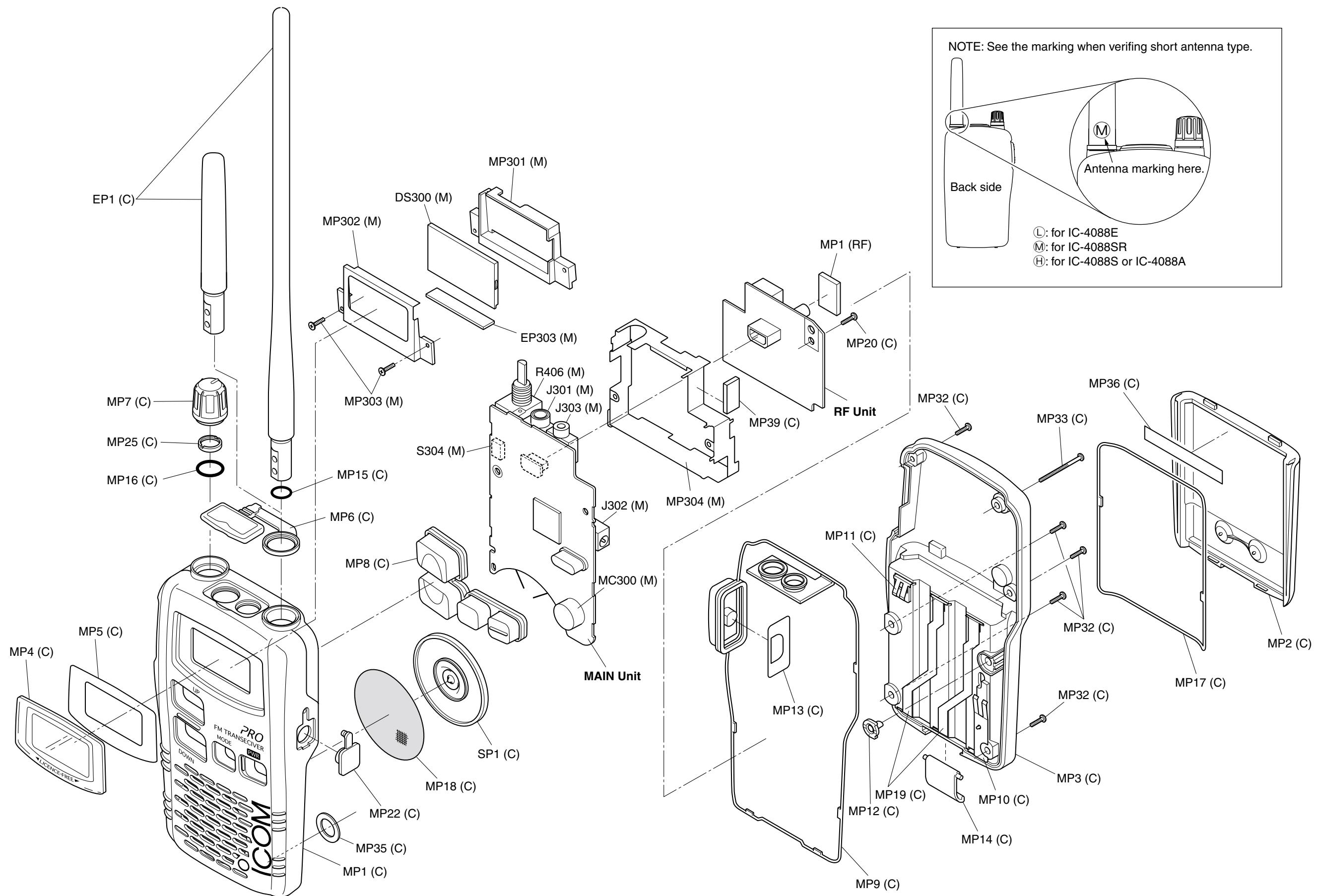
REF. NO.	ORDER NO.	DESCRIPTION	QTY.
MP1	8510011570	2405 VCO case	1

Screw abbreviations B0, BT: Self-tapping
 PH: Pan head
 ZK: Black

7-2 ACCESSORIES

REF. NO.	ORDER NO.	DESCRIPTION	QTY.
MP1	8930045332	2045 Belt clip-2	1

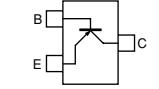
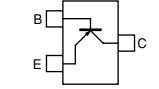
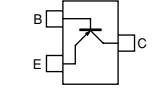
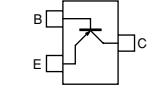
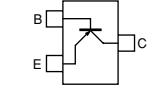
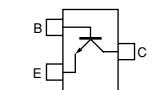
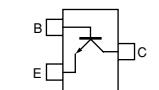
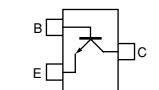
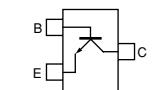
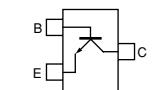
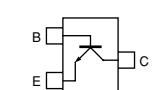
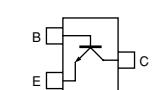
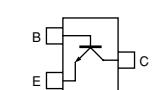
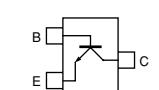
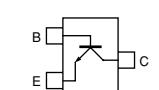
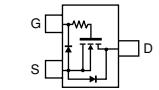
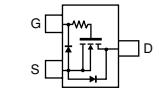
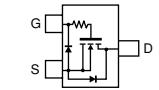
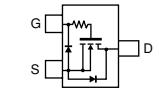
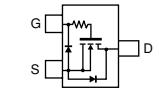
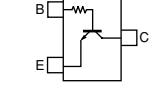
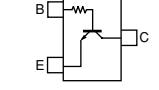
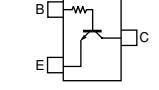
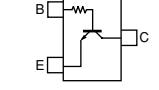
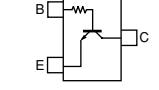
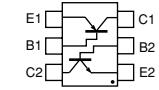




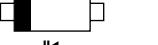
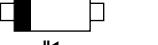
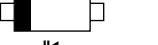
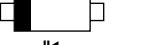
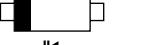
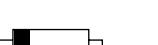
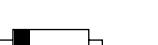
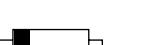
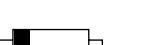
UNIT abbreviation (C): CHASSIS PARTS, (RF): RF UNIT, (M): MAIN UNIT

SECTION 8 SEMI-CONDUCTOR INFORMATION

• TRANSISTOR AND FET'S

2SA1362 GR (Symbol: AEG)	2SA1586 GR (Symbol: SG)	2SB798 DK (Symbol: DK)	2SB1132Q (Symbol: BA)	2SC3356 (Symbol: R22)
				
2SC4116GR (Symbol: LG)	2SC4117GR (Symbol: DG)	2SC4213 B (Symbol: AB)	2SC4215 O (Symbol: QO)	2SC4226 R25 (Symbol: R25)
				
2SC4228 R44 (Symbol: R44)	2SC4228 R45 (Symbol: R45)	2SC5108 Y (Symbol: MC)	2SJ144GR (Symbol: VG)	2SJ144 Y (Symbol: VY)
				
2SK1829 (Symbol: K1)	2SK3078A (Symbol: UW)	3SK320 (Symbol: U7)	DTA113ZU (Symbol: 111)	DTA114 EU (Symbol: 16)
				
DTC114TU (Symbol: 04)	DTC114YU (Symbol: 64)	DTC144EU (Symbol: 26)	HAT1023R (Symbol: 1023)	UMZ2N (Symbol: Z2)
				
XP4601 (Symbol: 5C)				
				

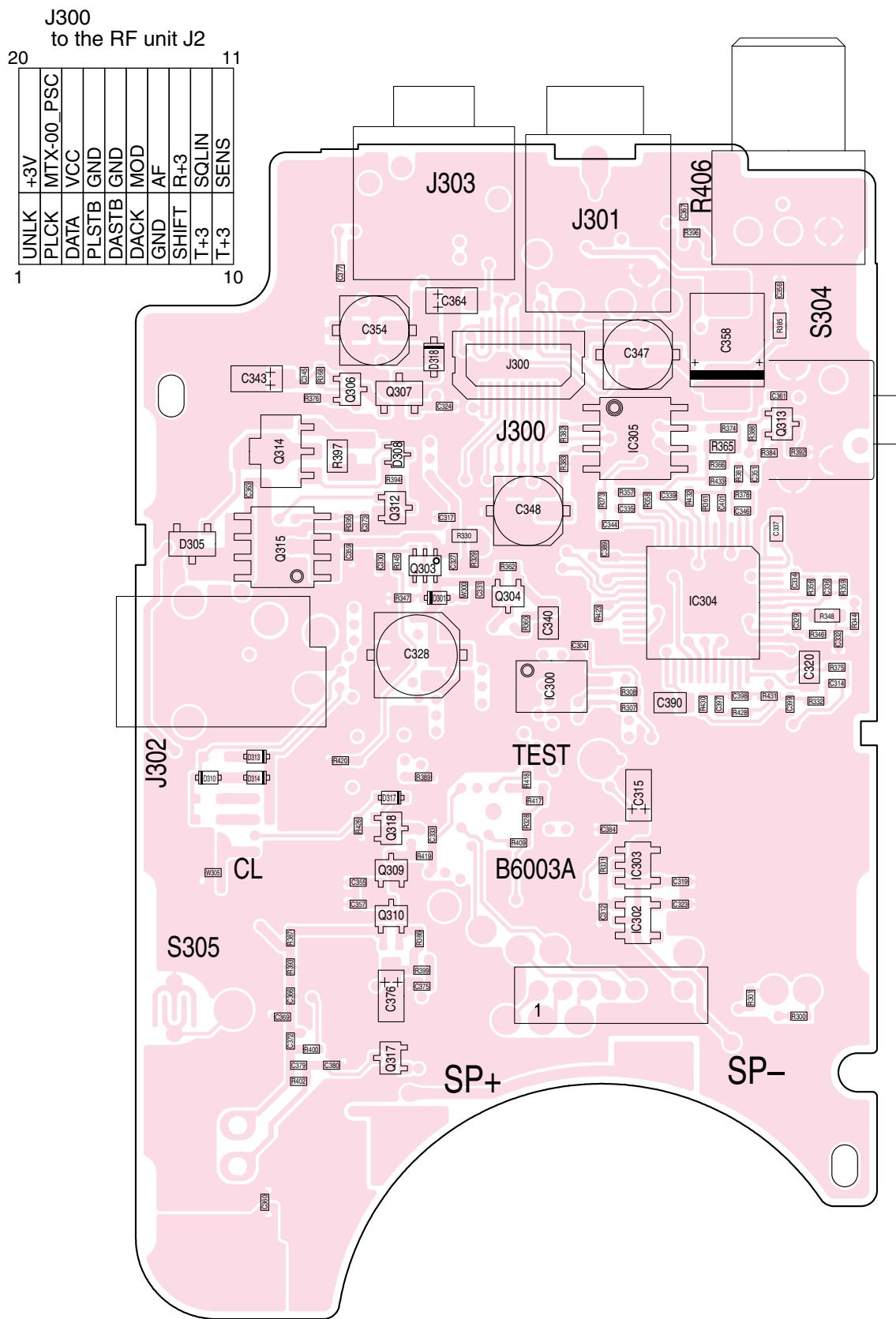
• DIODES

1SV284 (Symbol: TL)	1SV308 (Symbol: TX)	HVC376B (Symbol: B9)	MA2S077 (Symbol: S)	MA2S111 (Symbol: A)
				
MA2S728 (Symbol: B)	MA133 (Symbol: MP)	SB07-03C (Symbol: J)	RB551V-30 (Symbol: D)	
				

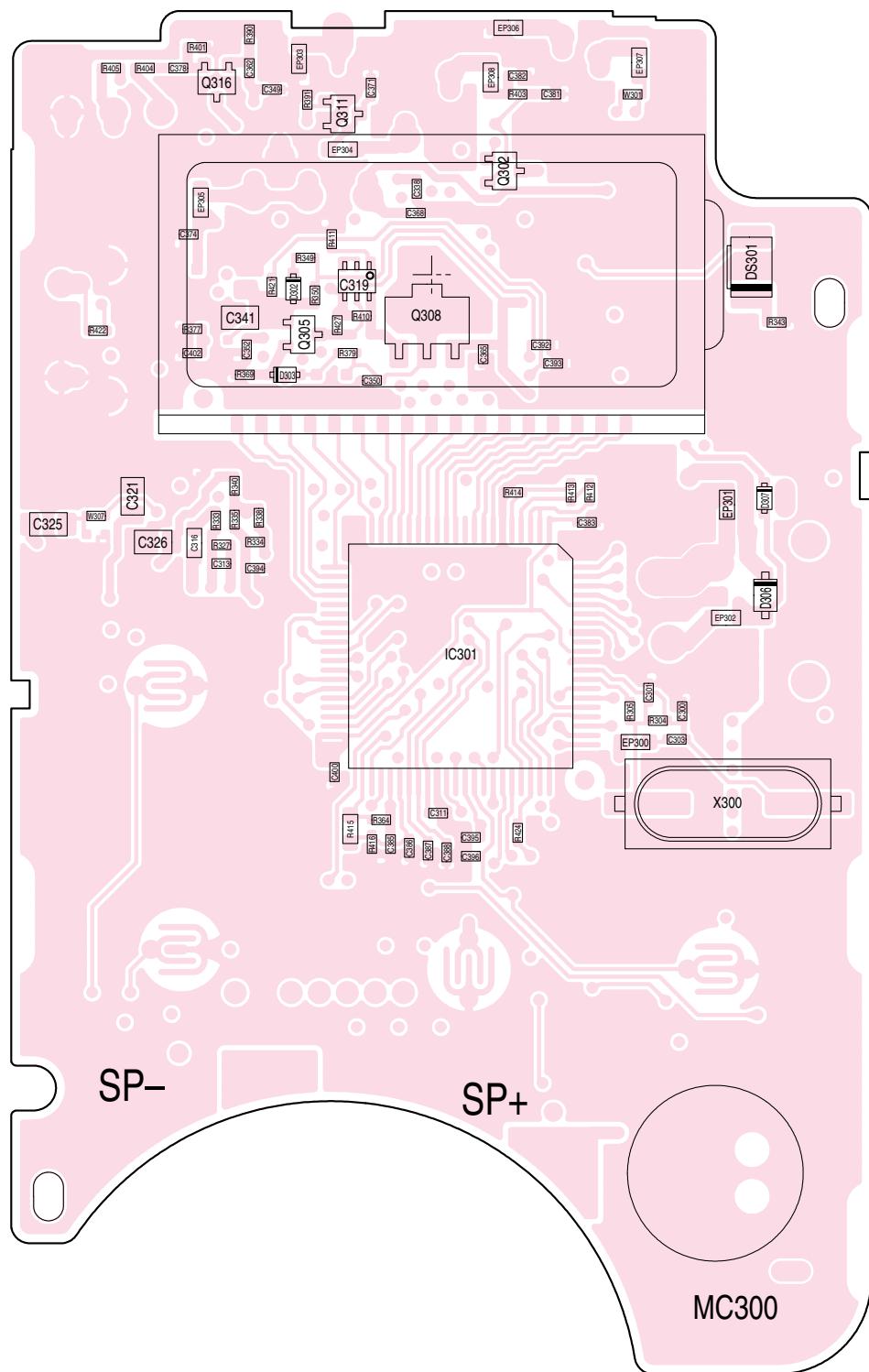
SECTION 9 BOARD LAYOUTS

9-1 MAIN UNIT

- TOP VIEW

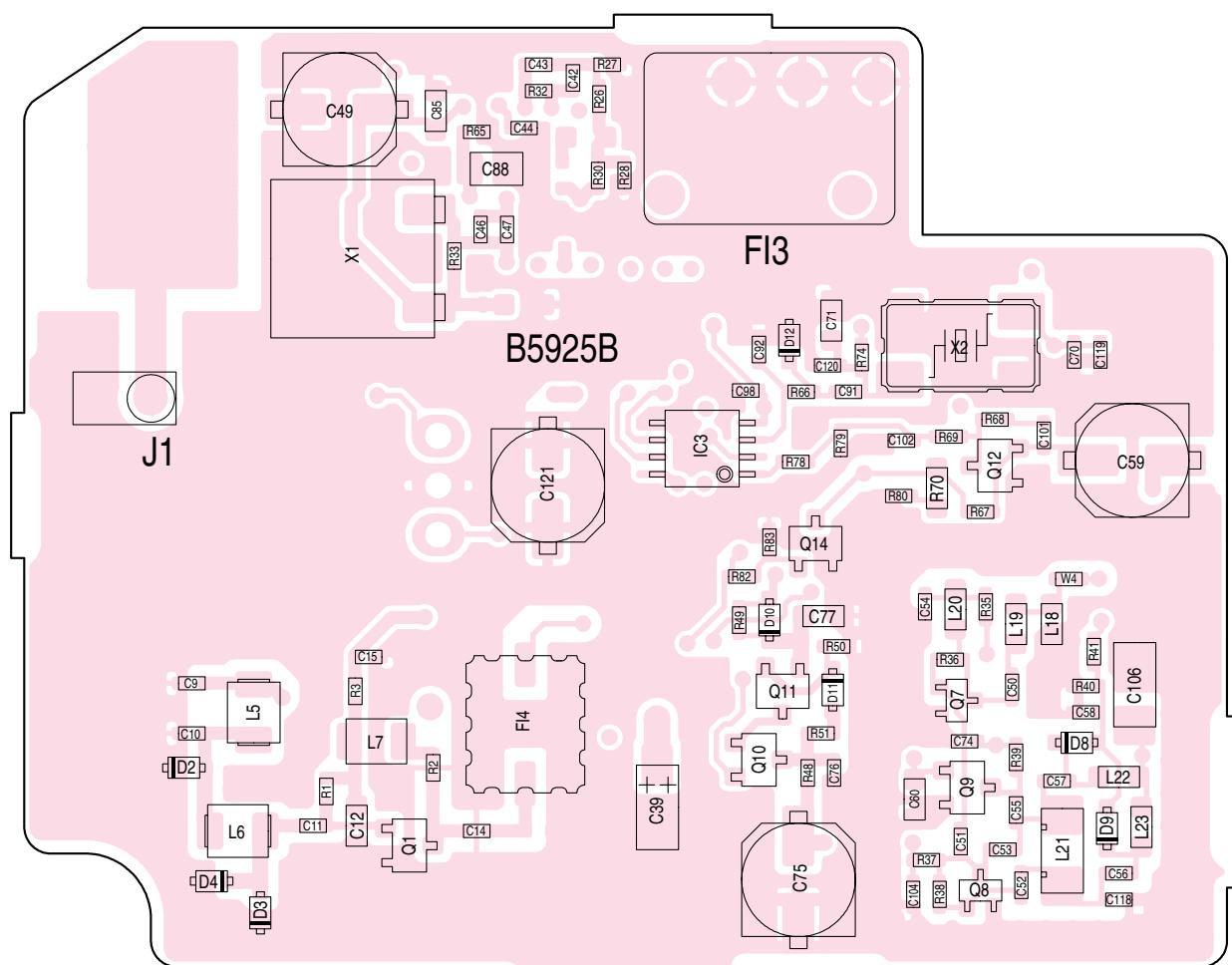


• BOTTOM VIEW

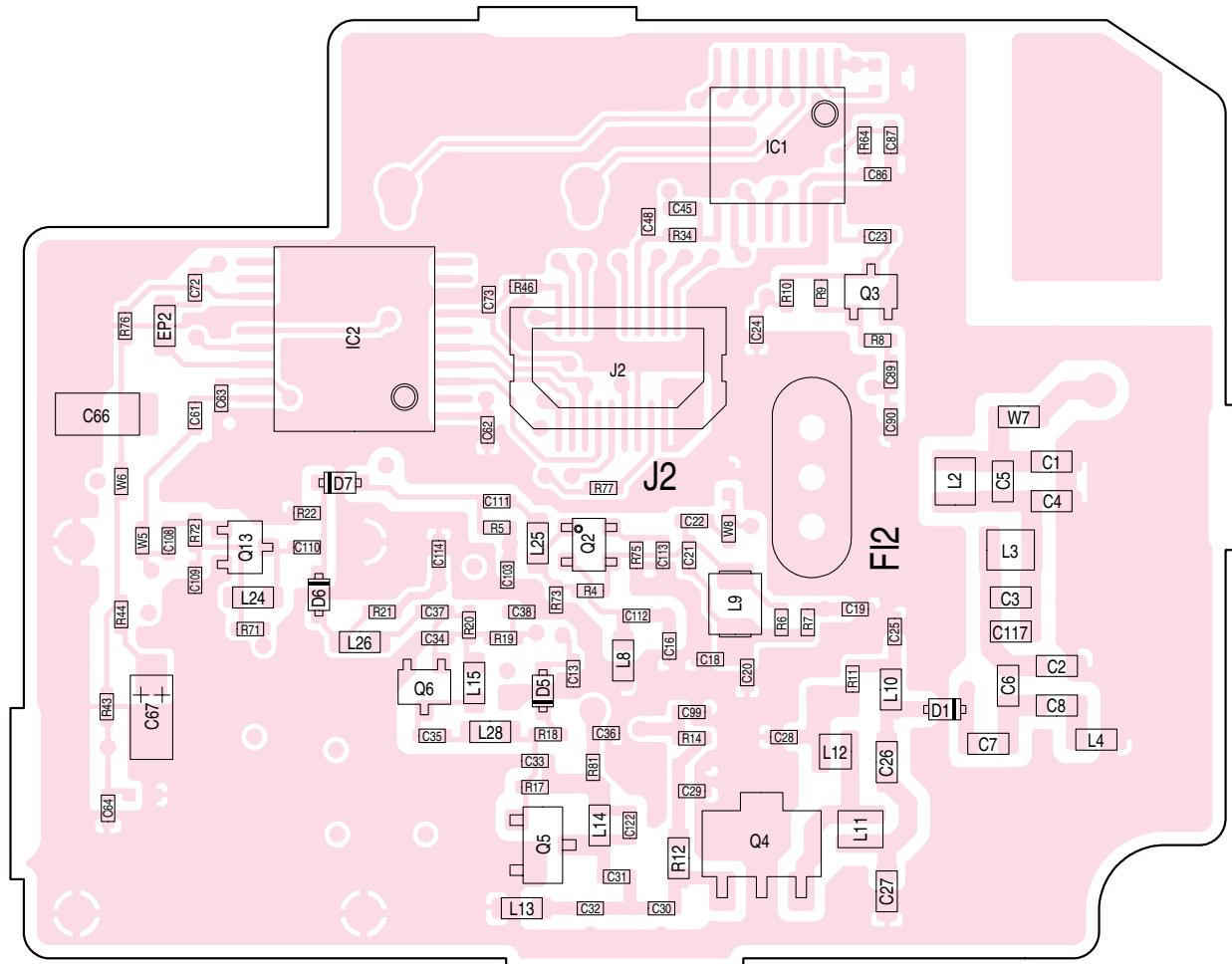
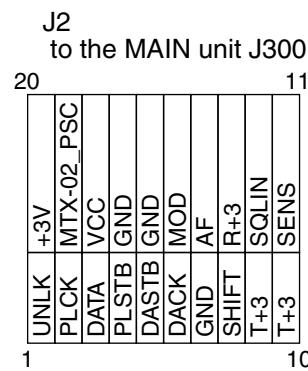


9-2 RF UNIT

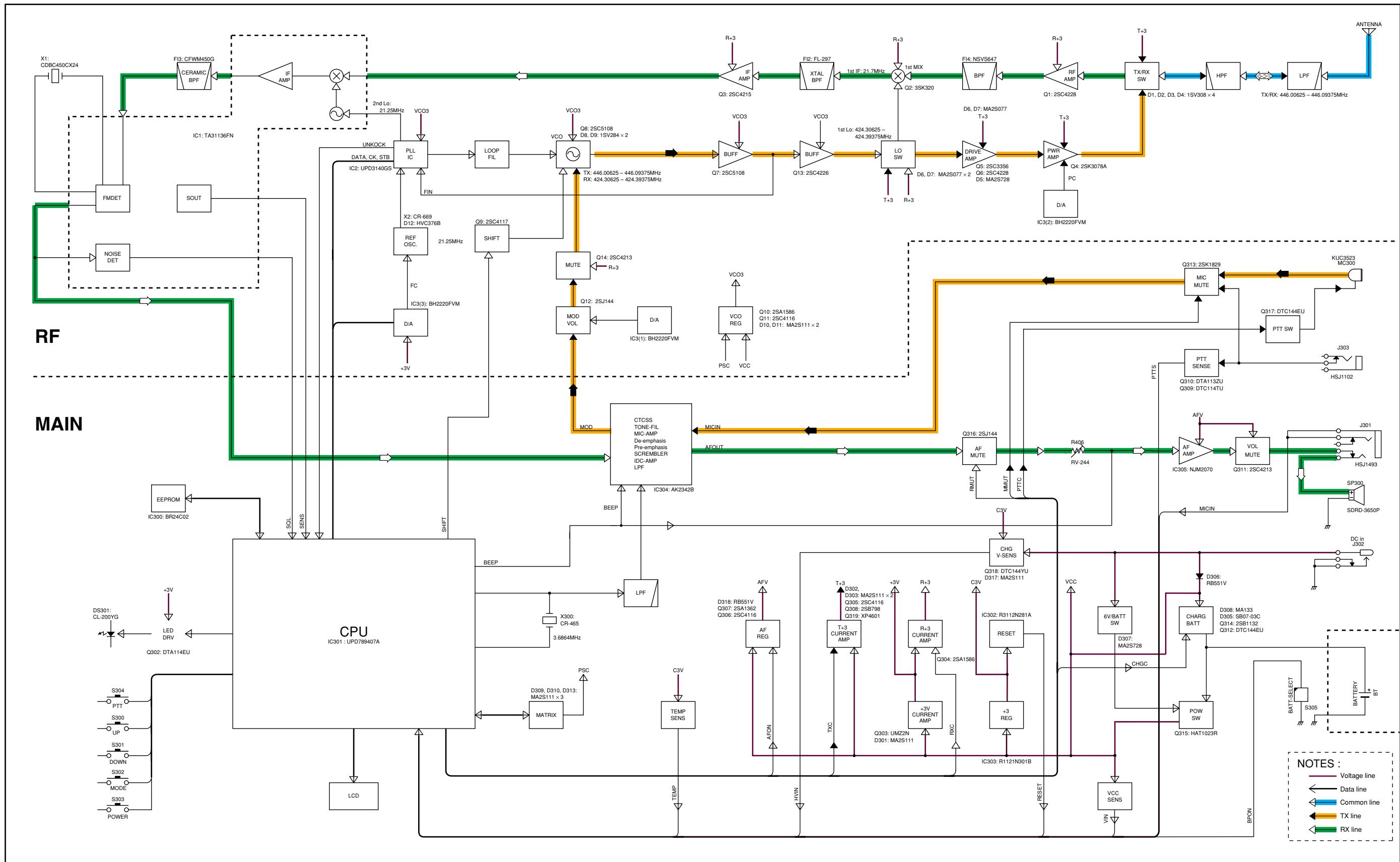
- TOP VIEW



• BOTTOM VIEW

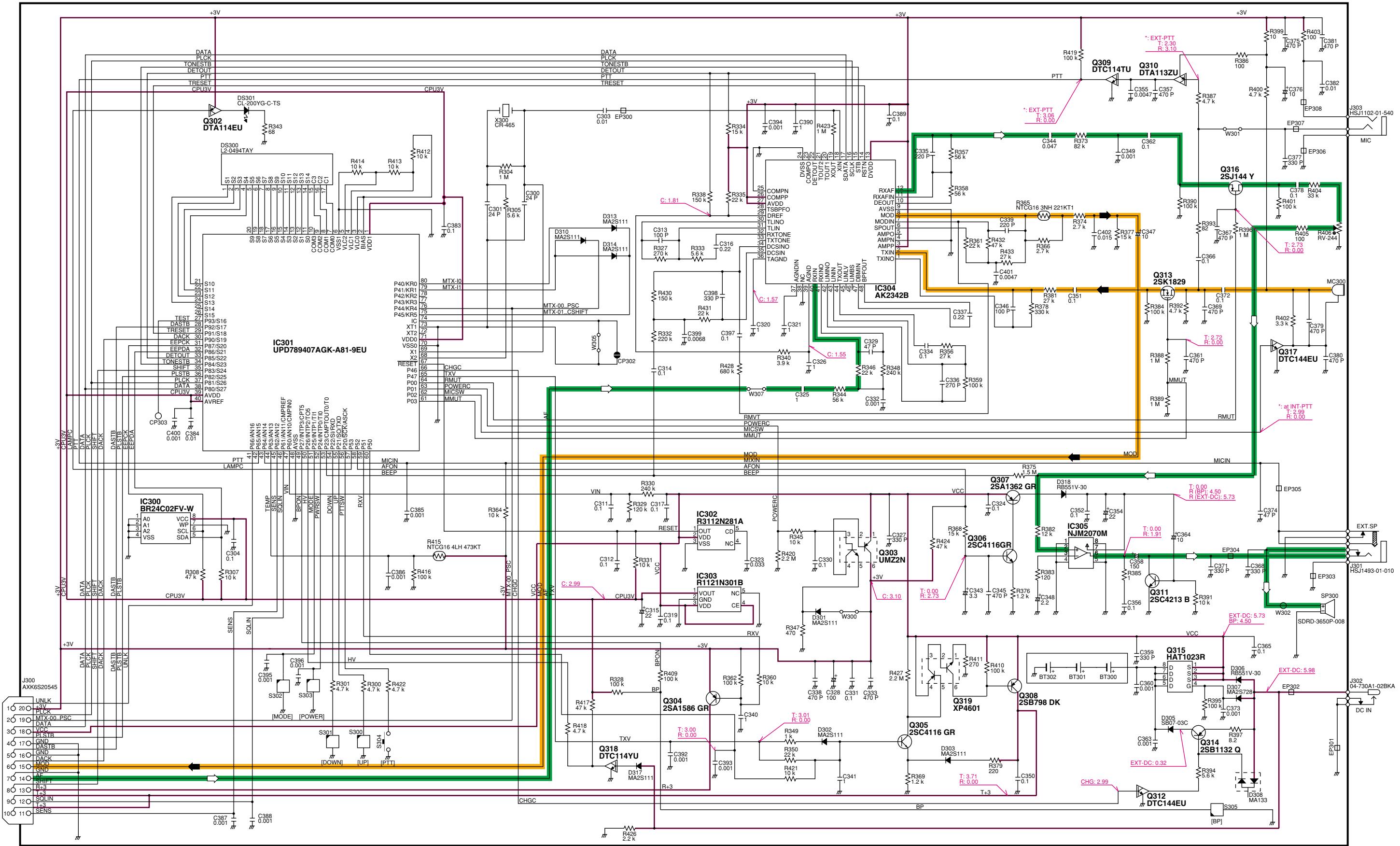


SECTION 10 BLOCK DIAGRAM



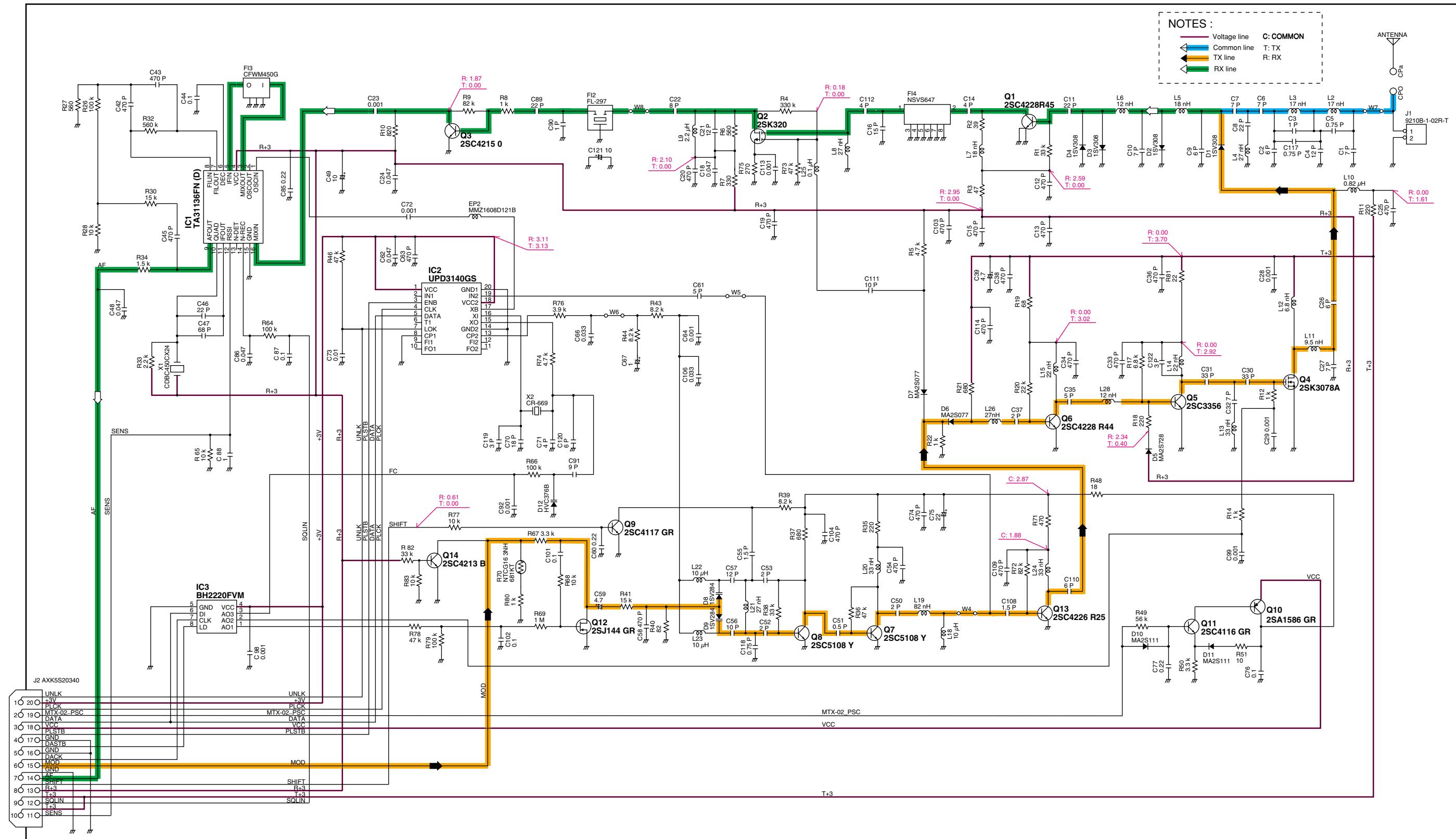
SECTION 11 VOLTAGE DIAGRAM

11-1 MAIN UNIT



11 - 1

11-2 RF UNIT



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